

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	920	transistor\$1 near5 ((programmable adj logic adj device\$1) PLD\$1 fpga\$1 cpld\$1)	USPAT	OR	OFF	2007/06/01 10:56
L2	5	(L1 near5 bias\$3).clm.	USPAT	OR	OFF	2007/06/01 10:56
L3	6356	(transistor\$1 fet\$1) near3 (reverse adj bias\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/01 10:59
L4	2	(L3 same ((programmable adj logic adj device\$1) PLD\$1 (logic adj chip))).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/01 10:59
L5	17	(reverse adj bias\$3) near5 (low adj power)	USPAT	OR	OFF	2007/06/01 11:00
L6	2	L5 near5 (transistor\$1 fet\$1)	USPAT	OR	OFF	2007/06/01 11:00
L7	14	L1 near5 bias\$3	USPAT	OR	OFF	2007/06/01 11:00
L8	2	L7 same (reverse forward)	USPAT	OR	OFF	2007/06/01 11:00
L9	0	L3 near3 ((programmable adj logic adj device\$1) PLD\$1 (logic adj chip))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/01 11:01
L10	4209	713/300,323,330,340.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/01 11:03
L11	0	l8 and l10	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/01 11:03
S1	545	transistor\$1 near5 ((programmable adj logic adj device\$1) PLD\$1)	USPAT	OR	OFF	2007/02/07 14:43

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S2	10	S1 near5 bias\$3	USPAT	OR	OFF	2007/02/07 14:44
S3	1	S2 same (reverse forward)	USPAT	OR	OFF	2007/02/09 15:22
S4	15	(reverse adj bias\$3) near5 (low adj power)	USPAT	OR	OFF	2007/02/07 13:34
S5	2	S4 near5 transistor\$1	USPAT	OR	OFF	2007/02/07 14:34
S6	2	S4 near5 (transistor\$1 fet\$1)	USPAT	OR	OFF	2007/02/07 14:34
S7	555	(fet\$1 transistor\$1) near5 ((programmable adj logic adj device\$1) PLD\$1)	USPAT	OR	OFF	2007/06/01 10:55
S8	0	S4 and S7	USPAT	OR	OFF	2007/02/07 14:35
S9	859	transistor\$1 near5 ((programmable adj logic adj device\$1) PLD\$1 fpga\$1 cpld\$1)	USPAT	OR	OFF	2007/02/07 14:44
S10	13	S9 near5 bias\$3	USPAT	OR	OFF	2007/06/01 10:55
S11	0	S10 near7 (reverse forward)	USPAT	OR	OFF	2007/02/07 14:45
S12	1	S10 same (reverse forward)	USPAT	OR	OFF	2007/02/07 14:45
S13	6273	(transistor\$1 fet\$1) near3 (reverse adj bias\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/09 15:25
S14	0	S13 near3 ((prorgammable adj logic adj device\$1) PLD\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/12 11:39
S15	39	S13 near5 ((low adj power) sleep idle standby)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/09 15:27
S16	6273	(transistor\$1 fet\$1) near3 (reverse adj bias\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/12 11:40

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S17	0	S16 near3 ((programmable adj logic adj device\$1) PLD\$1 (logic adj chip))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/12 11:41
S18	4	S16 same ((programmable adj logic adj device\$1) PLD\$1 (logic adj chip))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/01 10:59
S19	43	((programmable adj logic adj device\$1) PLD\$1 (logic adj chip)) near3 (transistor\$1 fet\$1) near3 (used using)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/12 14:19
S20	4	("6777978" "6621325").pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/12 14:27
S21	2	"6144357".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/12 14:27